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A customizable library to support software synthesis for embedded applications and micro-kernel systems

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Sintra, Portugal
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Author [Carsten Ditze](#)

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↳ D.4 [OPERATING SYSTEMS](#)

S. K. Reinhardt, J. R. Larus, D. A. Wood

April 1994 **ACM SIGARCH Computer Architecture News , Proceedings of the 21ST annual international symposium on Computer architecture**, Volume 22 Issue 2

Full text available:  pdf(1.44 MB) Additional Information: [full citation](#), [abstract](#), [references](#), [citations](#), [index terms](#)

Future parallel computers must efficiently execute not only hand-coded applications but also programs written in high-level, parallel programming languages. Today's machines limit these programs to a single communication paradigm, either message-passing or shared-memory, which results in uneven performance. This paper addresses this problem by defining an interface, *Tempest*, that exposes low-level communication and memory-system mechanisms so programmers and compilers can customize polici ...

9 A customizable library to support software synthesis for embedded applications and micro-kernel systems

Carsten Ditzel


September 1998 **Proceedings of the 8th ACM SIGOPS European workshop on Support for composing distributed applications**

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10 Coherence controller architectures for SMP-based CC-NUMA multiprocessors

Maged M. Michael, Ashwini K. Nanda, Beng-Hong Lim, Michael L. Scott

May 1997 **ACM SIGARCH Computer Architecture News , Proceedings of the 24th annual international symposium on Computer architecture**, Volume 25 Issue 2

Full text available:  pdf(1.56 MB) Additional Information: [full citation](#), [abstract](#), [references](#), [citations](#), [index terms](#)

Scalable distributed shared-memory architectures rely on coherence controllers on each processing node to synthesize cache-coherent shared memory across the entire machine. The coherence controllers execute coherence protocol handlers that may be hardwired in custom hardware or programmed in a protocol processor within each coherence controller. Although custom hardware runs faster, a protocol processor allows the coherence protocol to be tailored to specific application needs and may shorten ha ...

11 Verification of electronic systems

Alberto L. Sangiovanni-Vincentelli, Patrick C. McGeer, Alexander Saldanha

June 1996 **Proceedings of the 33rd annual conference on Design automation conference**

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12 Development of processors and communication networks for embedded systems: Component-based design approach for multicore SoCs

W. Cesário, A. Baghdadi, L. Gauthier, D. Lyonnard, G. Nicolescu, Y. Paviot, S. Yoo, A. A. Jerraya, M. Diaz-Nava

June 2002 **Proceedings of the 39th conference on Design automation**

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This paper presents a high-level component-based methodology and design environment for application-specific multicore SoC architectures. Component-based design provides primitives to build complex architectures from basic components. This bottom-up approach allows design-architects to explore efficient custom solutions with best performances. This paper presents a high-level component-based methodology and design environment for